



from which the perfect emerges

Wavelet Based Denoising

Katie Streit

Shannon Hughes

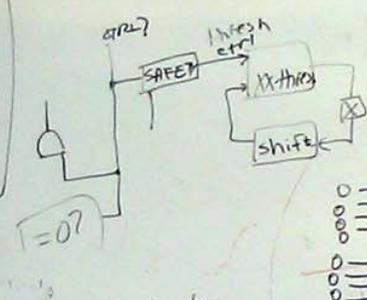
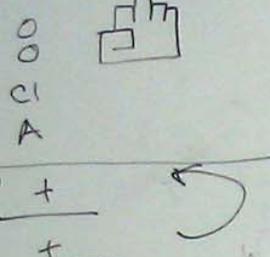
Roshan Mansinghani

Douglas Duncan

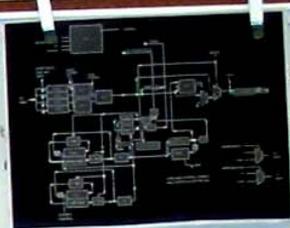
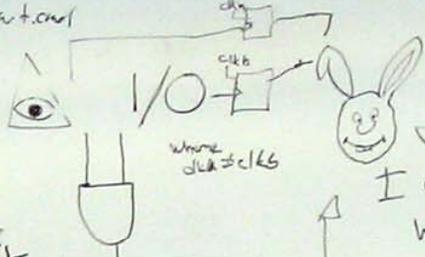
Where we left off

DD 4 LATCHES
LEAR SHIFTER

PLZ KILL ME!



- base case w/ thresh + cmd
- clipboth.cmd { clipfinal-ps } {
- thresh0.cmd { w/ thresh-ps } {
- lengthresh.cmd { large thresh-ps } {
- thresh0t.cmd { early exit-ps } {
- thresh2.cmd { thresh0-ps } {



Decomp. Problem:
Initialization:
[0 → 255 0 → 255
0 → 255 0 → 255
inductive step
if [-255 → 255 -
-255 → 255 -
then coeff = [-255 →
-255 →
always have
[-255 → 255 -
[-255 → 255 -
on decomp ⇒ no

- What we need to do to submit this f---ing thing:
- Finish testing:
- Fix adder probs
- Run chipboth
- run chipmiddle
- run a case w/ initial thresh=0
- run a 2x2 image
- run a case w/ a big thresh
- check that infunc can become whatever we would like
- repartition of labor
- pin outs
- final checks

Did we truncate to produce cameraman photos or did we actually MSB?

4 03c 255
204 01111111

if we have 4 legs + 4 th. precision (1 bits) in the

4-1004

reset in the middle of the image

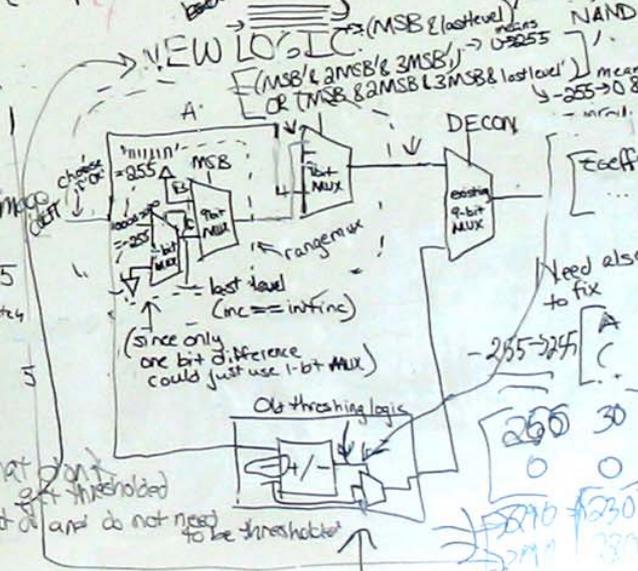
Reasons:
- just a key not a reset
- going to be in the middle of the image

(coeff < thres)
! (singl tower)
! sign = thres

scaling ⇒ 260

chiptest1

MSB (last level)
MSB & 2MSB & 3MSB ⇒ 0-255
OR (MSB & 2MSB & 3MSB & last level) means -255 → 0 & not on last level



Recon
- don't divide by 4 ⇒ potential

if we didn't expect to get a back upon recon why? b/c the coeffs for - level of n

so we should clip the c level w/o int error

BUT ⇒ on the last level clip to 8-b

[255 230
255 220]

- KT, holding a cable bottle
water had some issues turning

Englishly incorrect: "Doug
re than you" - Katie
asked him until the Sabbath
n number than me" - Doug about Post
Doug about error

Special aspects:
- neg coefficients
- wavelet coeffs that don't get thresholded and do not need to be thresholded

Wish I was smart + like Josh. But you know, God made me good looking. I guess you can't have everything" - Doug

Ob. threshing logic

Need also to fix

[260 30
0 0]

270 230
270 380

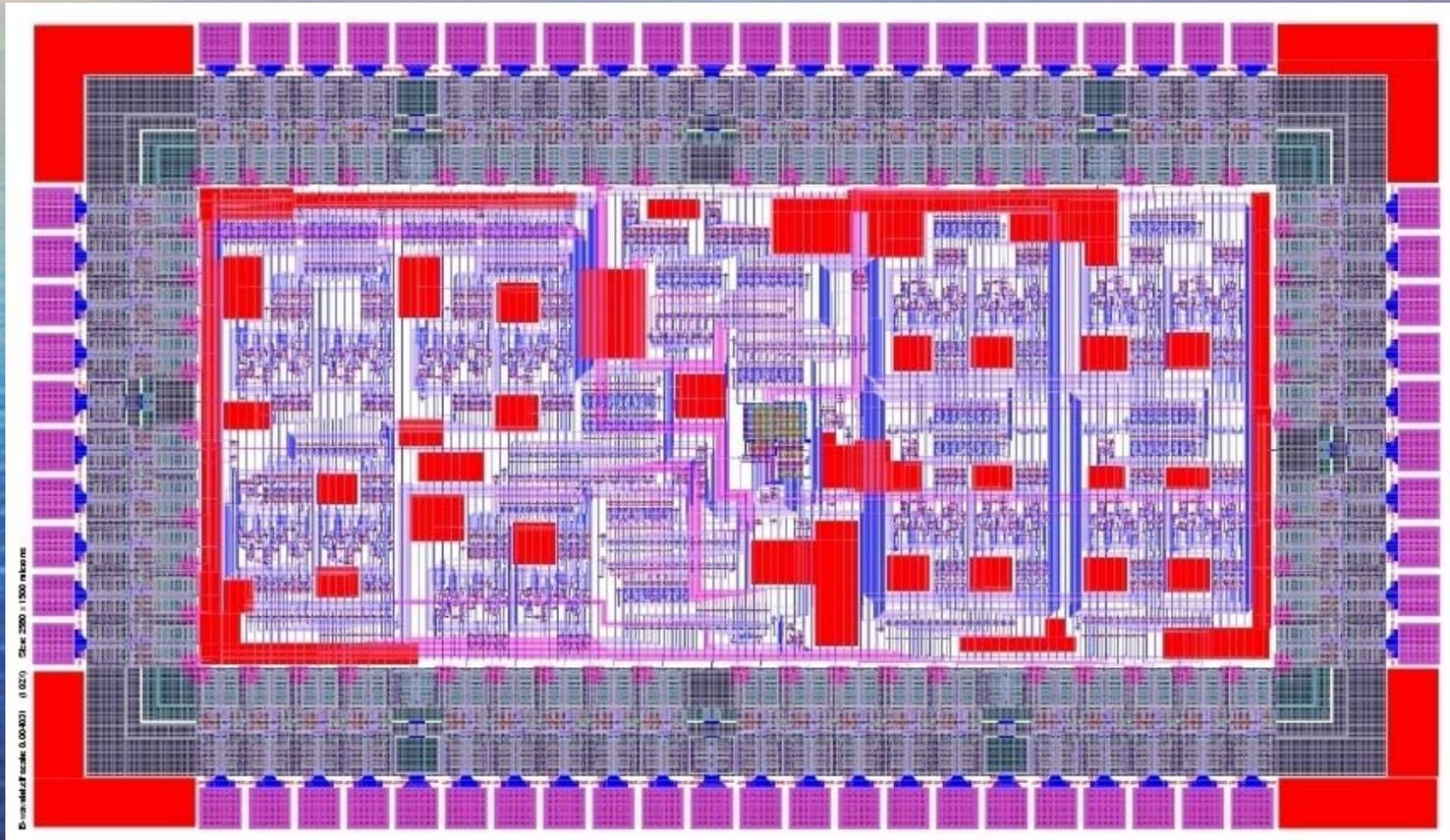
Chrysalis: our vision

- Wavelet denoising of grayscale images
- Compact, portable hardware implementation
- Fast enough to be practical
- Versatility in image sizes and choice of thresholds
- Interface with external RAM

Review of algorithm

- Multilevel wavelet transform
- Threshold small coefficients
- Inverse multilevel wavelet transform
- Clipping

Final floorplan

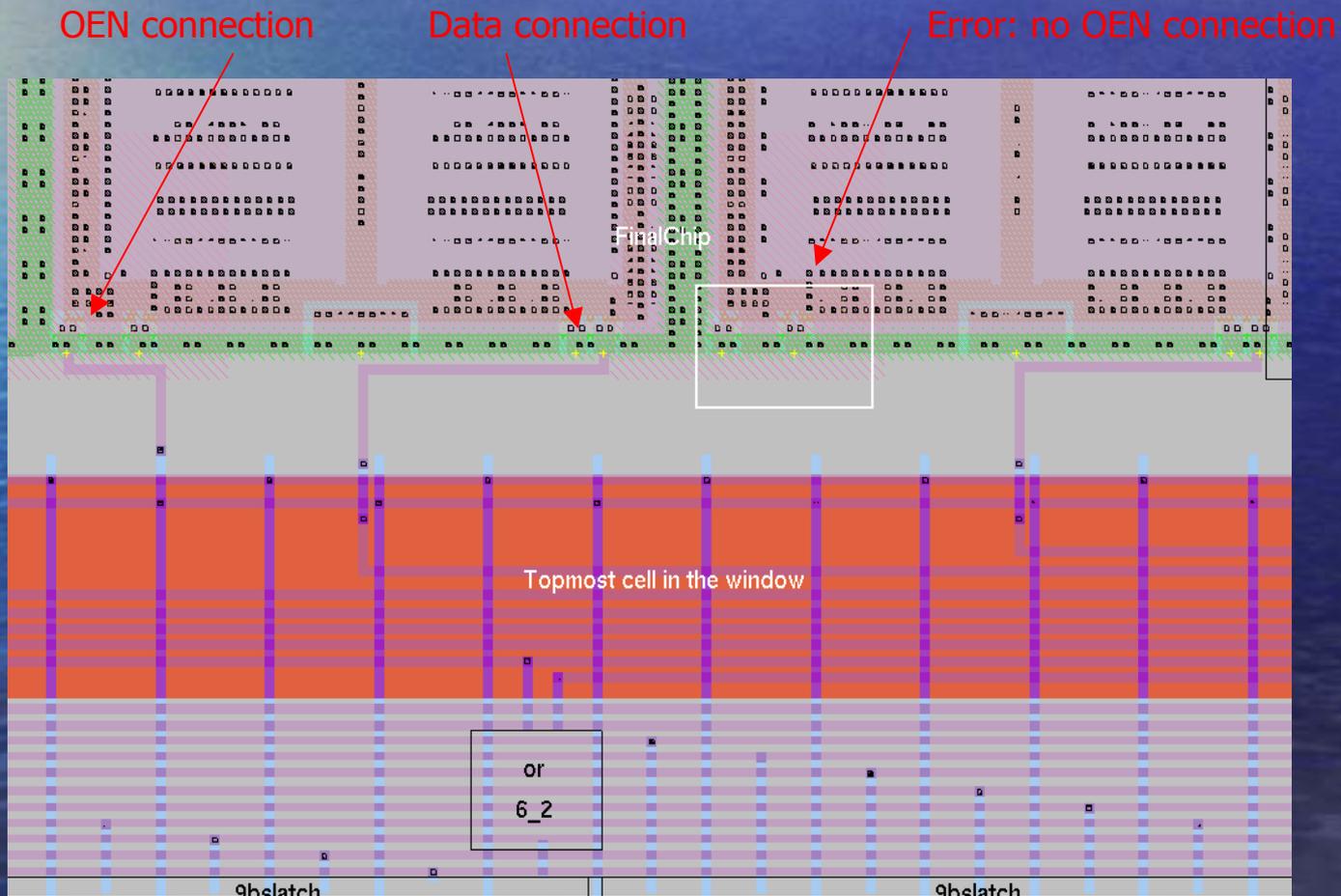


Testing setup

- Omnilab
 - Use our test images from last semester
 - 6 4x4 image tests and 1 2x2 image test
 - This part done with just the chip (no RAM)
- Tektronix
 - Interfacing with the RAM
 - Test a 2x2 image to warm up and then go for 256x256 cameraman image
 - Speed testing

Design errors found

- Floating OEN for Inc2 and Thresh2 pins



Design error details

- Consequence: Inc2 is grounded and Thresh2 is held high
- Cause: Wiring error
 - IRSIM simulation results didn't reflect errors
- Implications:
 - Reduction of image size possibilities to 2x2, 64x64, 128x128 and 256x256
 - Minimum threshold of 4 for image denoising

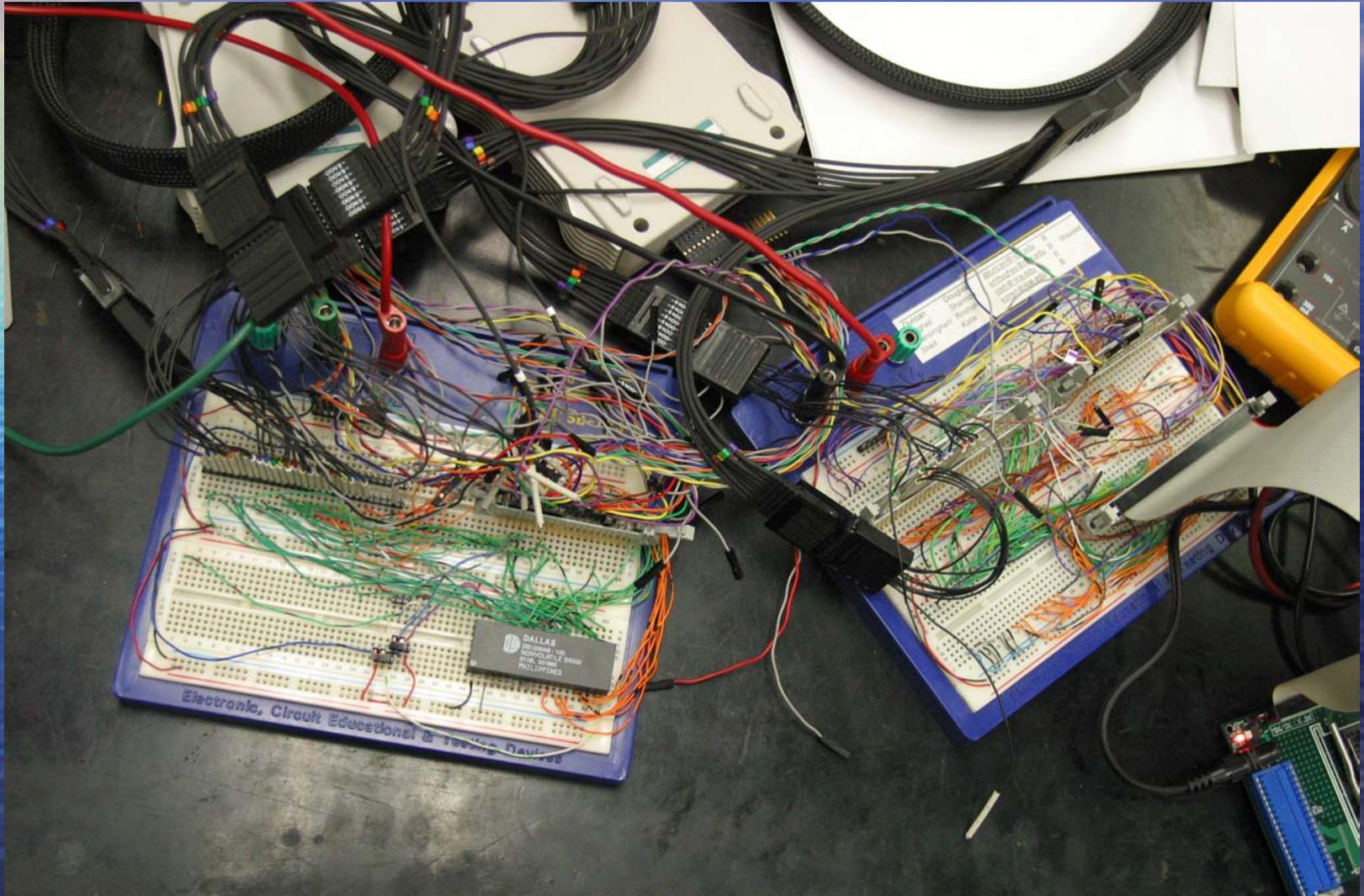
Omnilab testing results

- Despite design errors, we verified our ability to:
 - Initialize values for inc and thresh
 - Calculate correct RAM addresses for data
 - Assert RAM write and read enables when appropriate
 - Calculate wavelet transform coefficients including negative coefficients
 - Threshold when appropriate
 - Threshold ONLY when appropriate
 - Switch from deconstruction to reconstruction mode
 - Reconstruct pixel values from wavelet coefficients
 - Determine that we are done processing and stop

RAM challenges

- Hard to load an image into RAM
 - 256x256 image requires a command file 195,000 cycles long!
- Complicated wiring to interface between chip and RAM
- Running the chip on RAM requires a large command file
 - For a 256x256 image, this file must be 481,000 cycles long!
- Hard to get final data out of RAM for analysis
 - For 256x256 image, need to read out 65KB of data!

RAM Interface



RAM solutions

- Wrote Java program to convert data from Matlab into command file format for us
- Wrote additional Java program to generate command file for supplying inputs for chip with RAM testing
- Saved data out of Tektronix into file
- Wrote yet another Java program to parse this file into a Matlab-readable format

Another error

- While coefficients and addresses are both correct, they are not matching up.
- Reconstructed pixels get put back in the wrong order.
- Design error that is difficult to detect in cycle-by-cycle testing
- Probably fixable using external logic

Performance

- Mostly correct functionality for image denoising
 - Coefficients calculated correctly but stored in incorrect locations
 - Limited versatility in sizes and thresholds due to design errors
- Yield: 4 devices that work according to design

Speed testing results

- Maximum speed: 10 MHz
- Time for 256x256 image calculation:
0.10 seconds

Conclusions

- Still to do
 - Fix RAM interface
 - Full testing with RAM image
- Massively difficult project
- Testing results not perfect, but show promise

Any questions?

