

Elec 422, VLSI Design I
Fall 2001
Class Presentation Guidelines
AMD Design Contest Presentations

**Presentations: DH1070, Thursday December 6, 2001,
2:30-6:00pm**

Presentation Format

Each group will have about 10 minutes to make their presentation to the class. The group should prepare an electronic presentation with Powerpoint or Postscript/PDF which describes the project. Please make sure that the fonts are large enough to be readable. To show sections of the layout, please you use *cif2ps*, *pplot*, *xv*, *xwd*, or Xwin32 PC captured plots as part of the presentation. I will have a laptop and video projector available. Please e-mail me your presentation in advance.

Organization of Presentation

Each member of the group should discuss the section which is their respective specialty, and plan on a brief 3 minute overview per person. Due to the limited amount of time, you should present the key, unique features of your design. The following topics should be presented:

1. Functional Description of Chip and Project Completion Status.
2. Circuit Design
 - Logic Diagrams
 - System Timing Diagrams
3. PLA Description
 - State Transition Diagram
 - Input to meg, brief summary

4. Layout

- Plots of selected low-level Cells
- Cell Hierarchy
- Floorplan
- Full plot of Chip

5. System Performance Analysis

- Crystal Analysis of Longest Path
- Spice Analysis of Critical Sub-Circuit

6. Testability Analysis

- Functional Test Strategy
- Output Signals for Testability (State Bits, etc.)